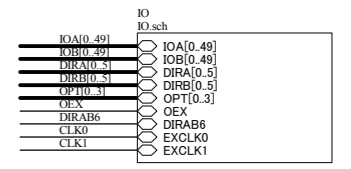


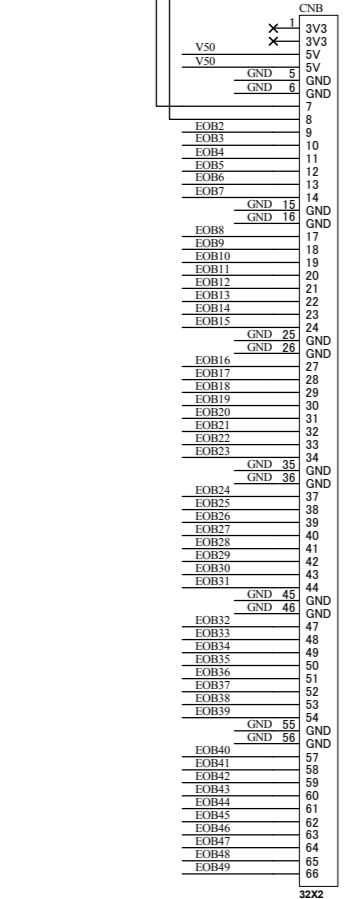
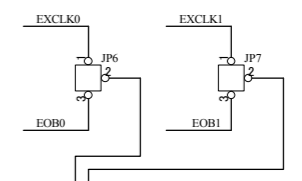
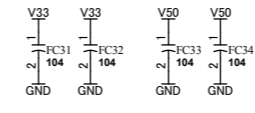
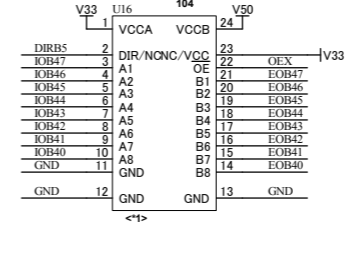
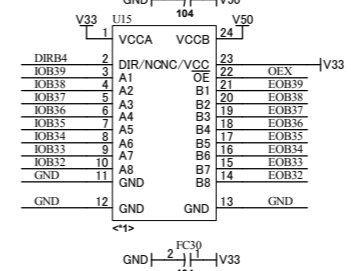
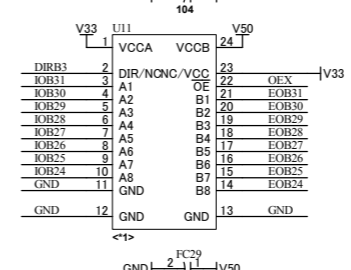
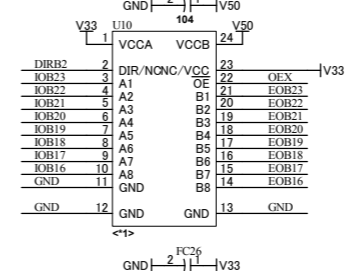
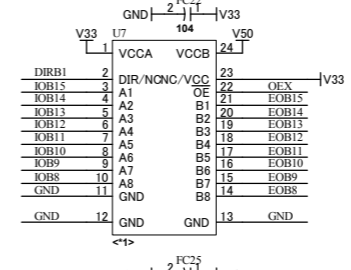
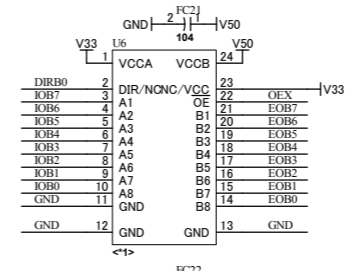
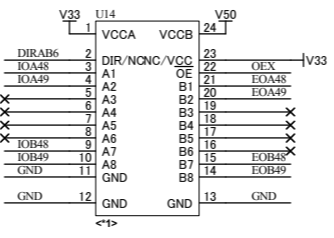
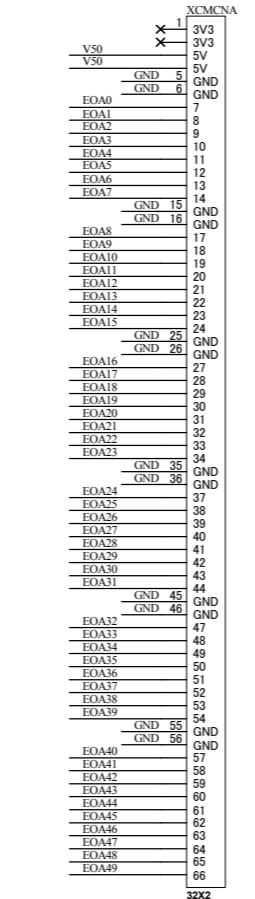
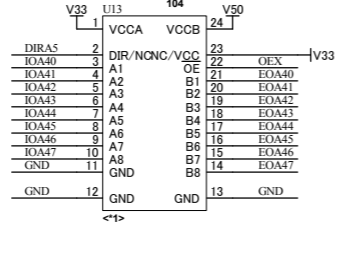
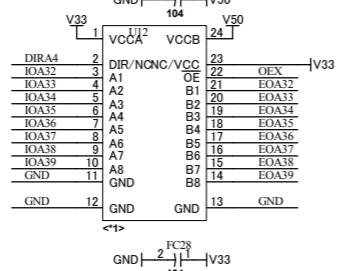
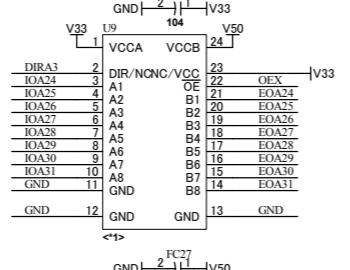
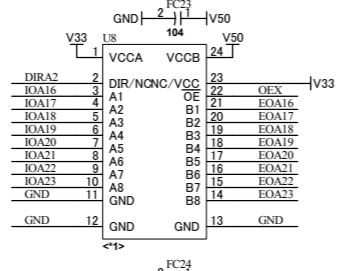
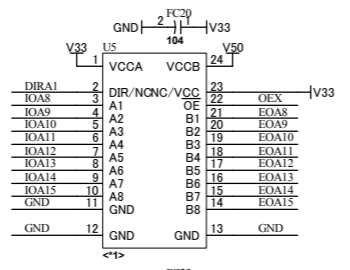
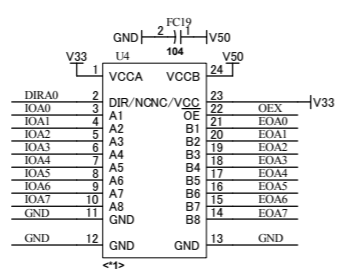
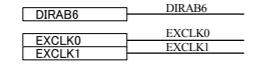
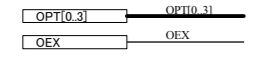
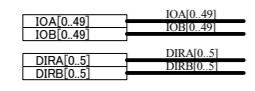
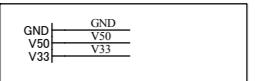
These I/O pins are connected to power. Users must need to set them as "input tri-stated".

These I/O pins are connected to power. Users must need to set them as "input tri-stated".



ACM012R2-SCH-C.pdf

		Altera Cyclone Q240 FPGA board (5V I/O)	
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U4-16	74LVCC3245	74CB3T3245
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<b>HUMAN DATA</b>		Altera Cyclone Q240 FPGA board (5V I/O)	
HuMANDATA LTD. OSAKA JAPAN www.hdl.co.jp/en/(Global) www.hdl.co.jp/(Japan)		DOC. No.	ACM-012
Date:	14-Feb-2014	13:48:51	Sheet 2 of 2