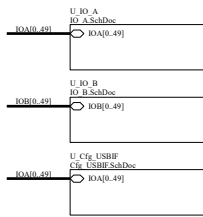
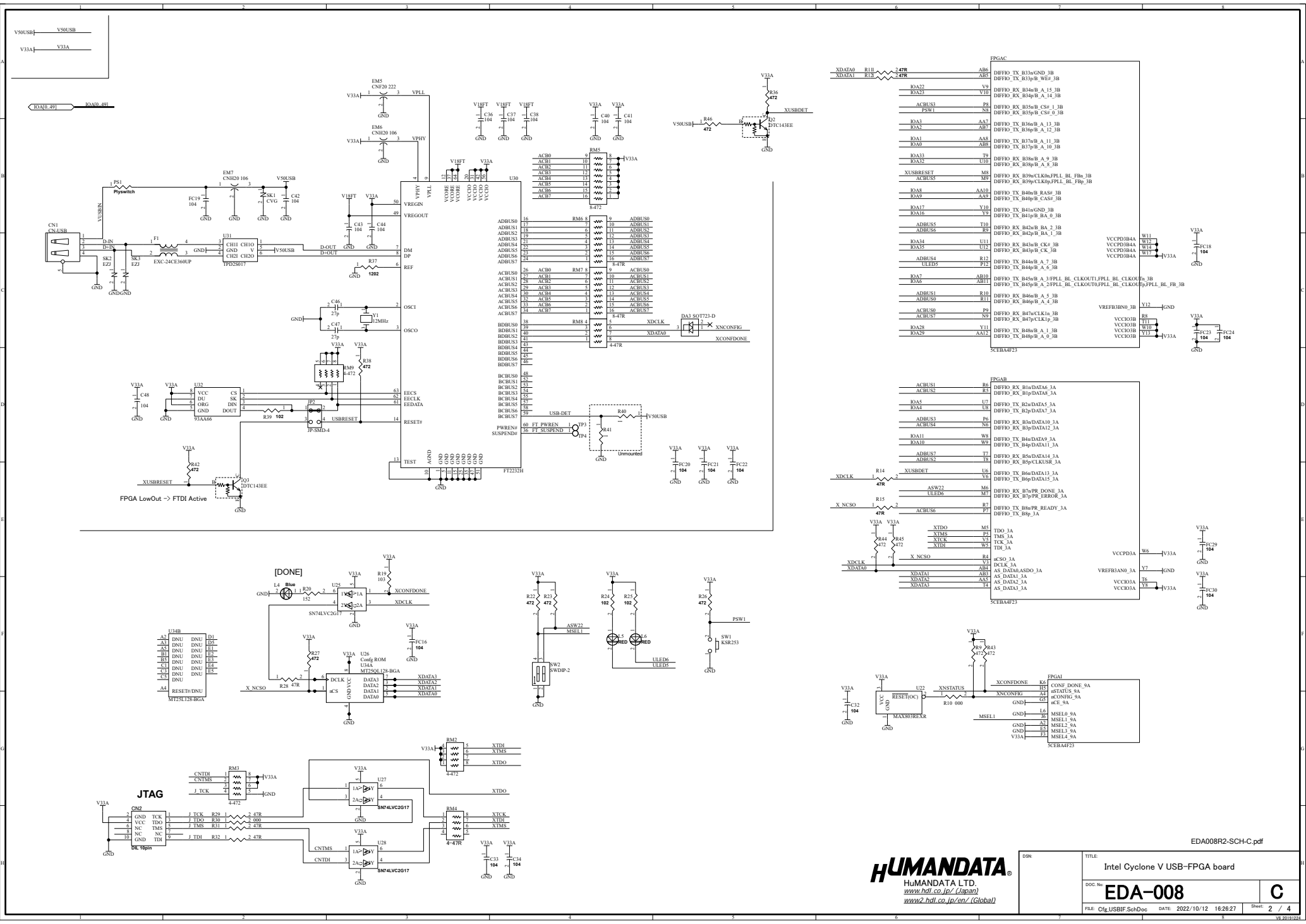


DIN:	TITLE: Intel CycloneV USB-FPGA board	
	DOC. No:	EDA-008
FILE: EDA008C.SchDoc	DATE: 2022/10/12 16:26:27	Sheet: 1 / 4



EDA008R2-SCH-C.pdf



FPGA		FPGA	
XDATA0	R11	47R	AB6
XDATA1	R12	47R	AB5
			DIFFO_TX_B16GND_3B
			DIFFO_RX_B16W_WE#_3B
	ROA22	V8	DIFFO_RX_B16W_A_15_3B
	ROA23	V10	DIFFO_RX_B16W_A_14_3B
	ACBUS1	P6	DIFFO_RX_B16W_CS#_1_3B
	PSW1	N6	DIFFO_RX_B16W_CS#_0_3B
	ROA3	AA5	
	ROA2	AB7	DIFFO_TX_B16W_A_1_3B
	ROA1	AA8	DIFFO_TX_B16W_A_12_3B
	ROA0	AB8	DIFFO_TX_B17W_A_10_3B
	ROA33	T9	DIFFO_RX_B16W_A_9_3B
	ROA32	U8	DIFFO_RX_B16W_A_8_3B
	XUSBRES1	M6	DIFFO_RX_B16W_A_7_3B
	ACBUS5	M5	DIFFO_RX_B16W_A_6_3B
	ROA8	AA10	DIFFO_TX_B46W_RASE_3B
	ROA9	AA9	DIFFO_TX_B46W_CAS#_3B
	ROA17	V10	DIFFO_TX_B41GND_3B
	ROA16	V8	DIFFO_TX_B41W_BA_0_3B
	ADDRESS1	T10	DIFFO_RX_B42W_BA_2_3B
	ADDRESS6	R8	DIFFO_RX_B42W_BA_1_3B
	ROA34	U11	
	ROA35	U12	DIFFO_RX_B46W_CKE_3B
	ADDRESS4	R12	DIFFO_TX_B46W_A_7_3B
	ULED5	P12	DIFFO_TX_B46W_A_6_3B
	ROA7	AB10	DIFFO_TX_B46W_A_3_FPLL_BL_CLKOUT1_FPLL_BL_CLKOUT
	ROA6	AB11	DIFFO_TX_B46W_A_2_FPLL_BL_CLKOUT0_FPLL_BL_CLKOUT
	ADDRESS1	R10	DIFFO_RX_B46W_B_A_5_3B
	ADDRESS2	R9	DIFFO_RX_B46W_B_A_4_3B
	ACBUS0	P8	DIFFO_RX_B47W_CLK1_3B
	ACBUS7	N8	DIFFO_RX_B47W_CLK0_3B
	ROA28	Y11	DIFFO_TX_B46W_B_A_1_3B
	ROA29	AA12	DIFFO_TX_B46W_B_A_0_3B

FPGA		FPGA	
ACBUS1	R6	DIFFO_RX_B16DATA6_3A	
ACBUS2	R5	DIFFO_RX_B16DATA5_3A	
ROA5	U7	DIFFO_TX_B26DATA5_3A	
ROA4	U8	DIFFO_TX_B26DATA4_3A	
ADDRESS1	P6	DIFFO_RX_B16DATA10_3A	
ACBUS4	N6	DIFFO_RX_B16DATA12_3A	
ROA11	W8	DIFFO_TX_B46DATA9_3A	
ROA10	W9	DIFFO_TX_B46DATA11_3A	
ADDRESS7	T7	DIFFO_RX_B56DATA4_3A	
ADDRESS2	T8	DIFFO_RX_B56CLKUSE_3A	
XDCLK	U6	DIFFO_TX_B66DATA13_3A	
X_NCSO	R15	DIFFO_TX_B66DATA15_3A	
	ASW22	M6	DIFFO_RX_B76PR_DONE_3A
	ULED6	M1	DIFFO_RX_B76PR_ERROR_3A
	X_NCSO	R1	DIFFO_TX_B86PR_READY_3A
	ACBUS6	P9	DIFFO_TX_B86PR_3A
	V33A	V33A	TDO_3A
	R44	R45	TMS_3A
	R47	R47	TCK_3A
	R47	R47	TDI_3A
	X_NCSO	R4	cS0_3A
	V33A	V33A	CLK1_3A
	ADDRESS1	AB4	AS_DATA0ASD0_3A
	ADDRESS2	AB5	AS_DATA1_3A
	ADDRESS3	AB6	AS_DATA2_3A
	ADDRESS4	AB7	AS_DATA3_3A

FPGA		FPGA	
XCONFONE	R6	CONF_DONE_9A	
XCONFREQ	R5	STATUS_9A	
RESETLOC	GND	cCONFNG_9A	
	GND	cCS_9A	
MSEL1	GND	MSEL1_9A	
	GND	MSEL1_9A	
	GND	MSEL1_9A	
	GND	MSEL1_9A	
	V33A	MSEL1_9A	

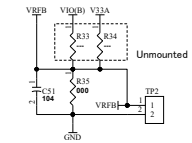
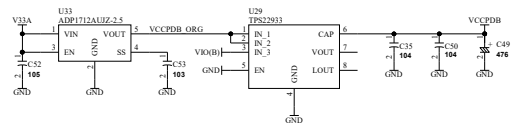


V33A
VIO(B)

R0B0_491

PGAG		FNGAH	
R0B19	K19	DIFFO_RX_T25nGND_7A	H21
R0B20	K20	DIFFO_RX_T25pGND_7A	H21
R0B21	C16	DIFFO_TX_T26nT_DQ_23_7A	H21
R0B22	D16	DIFFO_TX_T26pT_DM_2_7A	H21
R0B23	E16	DIFFO_RX_T27nT_DQ_20_7A	H21
R0B24	F16	DIFFO_RX_T27pT_DQ_21_7A	H21
R0B25	G16	DIFFO_TX_T28nGND_7A	H21
R0B26	H16	DIFFO_TX_T28nT_DQ_22_7A	H21
R0B27	I16	DIFFO_RX_T29nT_DQS_2_7A	H21
R0B28	J16	DIFFO_RX_T29pT_DQS_2_7A	H21
R0B29	K18	DIFFO_TX_T30nT_DQ_19_7A	H21
R0B30	L18	DIFFO_TX_T30pT_RESETP_7A	H21
R0B31	M15	DIFFO_RX_T31nT_DQ_16_7A	H21
R0B32	N15	DIFFO_RX_T31pT_DM_17_7A	H21
R0B33	O15	DIFFO_TX_T32nGND_7A	H21
R0B34	P15	DIFFO_TX_T32pT_DQ_18_7A	H21
R0B35	Q15	DIFFO_RX_T33nCLK1n_7A	H21
R0B36	R15	DIFFO_RX_T33pCLK1p_7A	H21
R0B37	S16	DIFFO_TX_T34nT_DQ_15_7A	H21
R0B38	T16	DIFFO_TX_T34pT_DM_17_7A	H21
R0B39	U15	DIFFO_RX_T35nT_DQ_12_7A	H21
R0B40	V15	DIFFO_RX_T35pT_DQ_13_7A	H21
R0B41	W14	DIFFO_TX_T36nT_CKE_0_7A	H21
R0B42	X15	DIFFO_TX_T36pT_DQ_14_7A	H21
R0B43	Y11	DIFFO_RX_T37nT_DQS_1_7A	H21
R0B44	Z11	DIFFO_RX_T37pT_DQS_1_7A	H21
R0B45	AA11	DIFFO_TX_T38nT_DQ_11_7A	H21
R0B46	AB11	DIFFO_TX_T38pT_CKE_17_7A	H21
R0B47	AC13	DIFFO_RX_T39nT_DQ_8_7A	H21
R0B48	AD13	DIFFO_RX_T39pT_DQ_9_7A	H21
R0B49	AE10	DIFFO_TX_T40nGND_7A	H21
R0B50	AF11	DIFFO_TX_T40pT_DQ_10_7A	H21
R0B51	AG13	DIFFO_RX_T41nCLK1n_7A	H21
R0B52	AH13	DIFFO_RX_T41pCLK1p_7A	H21
R0B53	AI12	DIFFO_TX_T42nT_DQ_7_7A	H21
R0B54	AJ12	DIFFO_TX_T42pT_DM_0_7A	H21
R0B55	AK13	DIFFO_RX_T43nT_DQ_4_7A	H21
R0B56	AL13	DIFFO_RX_T43pT_DQ_5_7A	H21
R0B57	AM12	DIFFO_TX_T44nT_ODT_1_7A	H21
R0B58	AN12	DIFFO_TX_T44pT_DQ_6_7A	H21
R0B59	AO11	DIFFO_RX_T45nT_DQS_0_7A	H21
R0B60	AP11	DIFFO_RX_T45pT_DQS_0_7A	H21
R0B61	AQ13	DIFFO_TX_T46nT_DQ_3_7A	H21
R0B62	AR13	DIFFO_TX_T46pT_ODT_0_7A	H21
R0B63	AS12	DIFFO_RX_T47nT_DQ_0_7A	H21
R0B64	AT12	DIFFO_RX_T47pT_DQ_1_7A	H21
R0B65	AV11	DIFFO_TX_T48nRZQ_2_7A	H21
R0B66	AW11	DIFFO_TX_T48pT_DQ_2_7A	H21

R0B67	AX10	DIFFO_RX_T49nCLKn_8A	H21
R0B68	AY10	DIFFO_RX_T49pCLKp_8A	H21
R0B69	AZ10	DIFFO_TX_T50nT_A_1_8A	H21
R0B70	BA10	DIFFO_TX_T50pT_A_8_8A	H21
R0B71	BB10	DIFFO_RX_T51nT_A_5_8A	H21
R0B72	BC10	DIFFO_RX_T51pT_A_4_8A	H21
R0B73	BD10	DIFFO_TX_T52nT_A_3FPPLL_TL_CLKOUT1_FPPLL_TL_CLKOUT1_8A	H21
R0B74	BE10	DIFFO_TX_T52pT_A_2FPPLL_TL_CLKOUT1_FPPLL_TL_CLKOUT1_8A	H21
R0B75	BF10	DIFFO_RX_T53nT_CKE_8A	H21
R0B76	BG10	DIFFO_RX_T53pT_CK_8A	H21
R0B77	BH10	DIFFO_TX_T54nT_A_7_8A	H21
R0B78	BI10	DIFFO_TX_T54pT_A_6_8A	H21
R0B79	BJ10	DIFFO_RX_T55nT_BA_2_8A	H21
R0B80	BK10	DIFFO_RX_T55pT_BA_1_8A	H21
R0B81	BL10	DIFFO_TX_T56nGND_8A	H21
R0B82	BM10	DIFFO_TX_T56pT_BA_0_8A	H21
R0B83	BN10	DIFFO_RX_T57nCLKnFPPLL_TL_FBn_8A	H21
R0B84	BO10	DIFFO_RX_T57pCLKpFPPLL_TL_FBp_8A	H21
R0B85	BP10	DIFFO_TX_T58nT_RAS_8A	H21
R0B86	BQ10	DIFFO_TX_T58pT_CAS_8A	H21
R0B87	BR10	DIFFO_RX_T59nT_A_9_8A	H21
R0B88	BS10	DIFFO_RX_T59pT_A_8_8A	H21
R0B89	BT10	DIFFO_TX_T60nT_A_11_8A	H21
R0B90	BV10	DIFFO_TX_T60pT_A_10_8A	H21
R0B91	BW10	DIFFO_RX_T61nT_CSE_1_8A	H21
R0B92	BX10	DIFFO_RX_T61pT_CSE_0_8A	H21
R0B93	BY10	DIFFO_TX_T62nT_A_13_8A	H21
R0B94	BZ10	DIFFO_TX_T62pT_A_12_8A	H21
R0B95	CA10	DIFFO_RX_T63nT_A_15_8A	H21
R0B96	CB10	DIFFO_RX_T63pT_A_14_8A	H21
R0B97	CC10	DIFFO_TX_T64nGND_8A	H21
R0B98	CD10	DIFFO_TX_T64pT_WB_8A	H21



EDA008R2-SCH-C.pdf



DIN:	TITLE:	Intel Cyclone V USB-FPGA board	
DOC. No.:	EDA-008	C	
FILE: 10_B_SchDoc	DATE: 2022/10/12 16:26:27	Sheet:	4 / 4